

Please type a plus sign (+) inside this box ☐

PTO/SB/21 (08-00)

Approved for use through 10/31/02. OMB 0651-0031
U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

TRANSMITTAL FORM

(to be used for all correspondence after initial filing)

Application Number	10/665,999
Filing Date	Sept. 17, 2003
First Inventor	John R. Allen
Confirmation No.	8962
Group Art Unit	2124
Examiner Name	Chuong D. Ngo
Attorney Docket No.	CAT004 US

Total Number Of Pages In This Submission

4+(3 ref)

ENCLOSURES (check all that apply)

- | | | |
|--|---|---|
| <input type="checkbox"/> Fee Transmittal Form
(1 page in duplicate) | <input type="checkbox"/> Assignment Papers
(for an Application) | <input type="checkbox"/> After Allowance Communication to Group |
| <input type="checkbox"/> Fee Attached -- Credit
Card Payment Form (1 pg) | <input type="checkbox"/> Drawing(s) | <input type="checkbox"/> Appeal Communication to Board of
Appeals and Interferences |
| <input type="checkbox"/> Amendment / Reply | <input type="checkbox"/> Licensing-related Papers | <input type="checkbox"/> Appeal Communication to Group
(Appeal Notice, Brief, Reply Brief) |
| <input type="checkbox"/> After Final | <input type="checkbox"/> Petition | <input type="checkbox"/> Proprietary Information |
| <input type="checkbox"/> Affidavits/declarations | <input type="checkbox"/> Petition to Convert to a
Provisional Application | <input type="checkbox"/> Status Letter |
| <input type="checkbox"/> Extension of Time Request | <input type="checkbox"/> Power of Attorney, Revocation of
Previous Powers; And Statement
Under 37 CFR 3.73(b) | <input checked="" type="checkbox"/> Other Enclosure(s) (please identify
below): |
| <input type="checkbox"/> Express Abandonment Request | <input type="checkbox"/> Terminal Disclaimer | RETURN RECEIPT POSTCARD
PTO Form 1449 (1 page)
Copies of 3 Cited References |
| <input checked="" type="checkbox"/> Information Disclosure Statement (2
pages) | <input type="checkbox"/> Request for Refund | |
| <input type="checkbox"/> Certified Copy of Priority Document(s) | <input type="checkbox"/> CD, Number of CD(s) _____ | |
| <input type="checkbox"/> This is a Response to Missing Parts/
Incomplete Application under 37 CFR
1.52 or 1.53 | Remarks | |
| <input type="checkbox"/> Copy of Notice To File Missing
Parts (2 pages) | Please charge Deposit Account 50-2263 for any underpaid fee. | |

SIGNATURE OF APPLICANT, ATTORNEY OR AGENT

Firm or Individual Name	Omkar K. Suryadevara (Reg. No. 36,320) Silicon Valley Patent Group LLP 2350 Mission College Boulevard, Suite 360 Santa Clara, California 95054
Signature	<i>S. Omkar</i>
Date	January 14, 2005

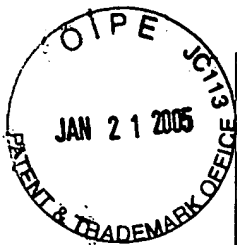
CERTIFICATE OF MAILING BY "FIRST CLASS"

I hereby certify that this paper or fee is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the below date.

S. Omkar
Omkar K. Suryadevara

January 14, 2005
Date

Burden Hours Statement: This form is estimated to take 0.2 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Arlington, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Arlington, VA 22313-1450.



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventors: John R. Allen;
Assignee: Catalytic, Inc.
Title: Emulation Of A Fixed Point Operation Using A Corresponding Floating Point Operation
Serial No.: 10/665,999 Filing Date: Sept. 17, 2003
Examiner: Chuong D. Ngo Group Art Unit: 2124
Docket No.: CAT004 US Confirmation No: 8962

Santa Clara, California
January 14, 2005

MAIL STOP AMENDMENT
COMMISSIONER FOR PATENTS
P.O. BOX 1450
ALEXANDRIA, VA 22313-1450

**INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR §1.97(b)**

Dear Sir:

Pursuant to 37 C.F.R. § 1.56, §1.97 and §1.98, the Applicants submit for consideration in the above-identified patent application the document listed on the accompanying Form PTO-1449. Copies of references numbered 4-6 are submitted herewith. The Examiner is requested to make these documents of record. The remaining references are not attached hereto, because these references are issued patents or publications which are readily available in the U.S. Patent and Trademark Office.

This Information Disclosure Statement is submitted pursuant to 37 CFR §1.97(b) as it within three months of the filing date of a national application other than a continued prosecution application and/or before the mailing of a first Office Action on the merits. Accordingly, no fee is required.

Applicants would appreciate the Examiner initialing and returning the Form PTO-1449, indicating that the information has been considered and made of record herein.

SILICON VALLEY
PATENT GROUP LLP
2350 Mission College Blvd.
Suite 360
Santa Clara, CA 95054
(408) 982-8200
FAX (408) 982-8210

The information contained in this Information Disclosure Statement is to the best of my knowledge and is not to be construed as a representation that: (i) a complete search has been made; (ii) additional information material to the examination of this application does not exist; (iii) the information, protocols, results and the like reported by third parties are accurate or enabling; or (iv) the above information constitutes prior art to the subject invention.

CERTIFICATE OF MAILING BY "FIRST CLASS"

I hereby certify that this paper or fee is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231 on January 14, 2005.

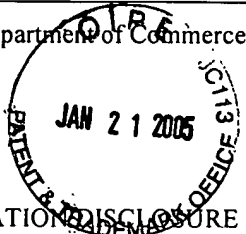


Omkar K. Suryadevara

Respectfully submitted,



Omkar K. Suryadevara
Attorney for Applicants
Reg. No. 36,320

U.S. Department of Commerce, Patent and Trademark Office  INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)	Application No.:	10/665,999
	Filing Date:	Sept. 17, 2003
	First Named Inventor:	John R. Allen
	Group Art Unit:	2124
	Examiner Name:	Unknown
	Confirmation No.:	8962
	Attorney Docket No.:	CAT004 US

U.S. Patent Documents							
*Examiner Initials		Document Number	Date	Name	Class	Subclass	Filing Date if Appropriate
	1.	6,460,177	10/01/02	Lee	717	146	
	2.	6,173,247	01/09/01	Maurudis et al.	703	23	
	3.	5,222,037	06/22/93	Taniquchi	364	748	

Foreign Patent Documents							Translation	
		Document	Date	Country	Class	Subclass	Yes	No

Other Art (Including Author, Title, Date, Pertinent Pages, Etc.)		
	4.	P. Banerjee, "An Overview of the AccelFPGA Compiler for Mapping MATLAB Programs onto FPGAs", Prith Banerjee – UC Berkeley Talk, Nov. 6, 2002, pp. 1-33
	5.	M. Groden, "Automatic RTL Conversion of DSP Algorithms for a Channelized Wideband Receiver", Int'l Signal Processing Conference, April 2003, Dallas, TX, pp. 1-5
	6.	"Filter Design Toolbox" For Use with MATLAB, User's Guide, Version 2, Chapter 5 "Quantization and Quantized Filtering" pp. 5.2-5.26, Chapter 6, "Working with Objects" pp. 6.1-6.10, Chapter 7, "Quantizers and Unit Quantizers, pp. 7.2-7.10, Chapter 8, "Working with Quantized Filters", pp. 8.1-8.16, Chapter 9, "Working with Quantized FFTs", pp. 9.1-9.10, Chapter 10, "Quantized Filtering Analysis Examples", pp. 10.1-10.26, June, 2001

Examiner:	Date Considered:
* Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication with applicant.	